Plasma etching of wide bandgap and ultrawide bandgap semiconductors

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Citation: Journal of Vacuum Science & Technology A 38, 020802 (2020); doi: 10.1116/1.5131343

View online: https://doi.org/10.1116/1.5131343

View Table of Contents: https://avs.scitation.org/toc/jva/38/2

Published by the American Vacuum Society





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Cite as: J. Vac. Sci. Technol. A 38, 020802 (2020); doi: 10.1116/1.5131343 Submitted: 10 December 2019 · Accepted: 27 December 2019 · Published Online: 16 January 2020







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Note: This paper is part of the Special Topic Collection Commemorating the Career of John Coburn.

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ABSTRACT

The precise patterning of front-side mesas, backside vias, and selective removal of ternary alloys are all needed for power device fabrication in the various wide bandgap (AlGaN/GaN, SiC) and ultrawide bandgap (high Al-content alloys, boron nitride, Ga2O3, diamond) semiconductor technologies. The plasma etching conditions used are generally ion-assisted because of the strong bond strengths in these materials, and this creates challenges for the choice of masks in order to have sufficient selectivity over the semiconductor and to avoid mask erosion and micromasking issues. It can also be challenging to achieve practical etch rates without creating excessive damage in the patterned surface. The authors review the optimum choices for plasma chemistries for each of the semiconductors and acknowledge the pioneering work of John Coburn, who first delineated the ion-assisted etch mechanism.

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I. INTRODUCTION

The two wide bandgap semiconductors that are commercialized are SiC and GaN, both of which are used in devices for power switching and power amplifier applications. 1-20 There are also emerging markets for lidar sensors for autonomous vehicles, multilevel converters, and motion control for robotics. Power electronics are responsible for controlling and converting electrical power to provide optimal conditions for transmission, distribution, and loadside consumption. 6-8 High-voltage switching transistors used in these applications are required to have small ON resistance while providing very high blocking voltages in the OFF state. 14-16 Achieving high power conversion efficiency requires low loss power semiconductor switches. 1-3 Power semiconductor devices are used in three-terminal switches or two-terminal rectifiers, when forward biased should have minimal resistance in the on-state, R_{on-sp} , and should support a large blocking voltage, $V_{\rm B}$, in the off-state. In a standard device design, $^{5-10}$ increasing the thickness, L_N , or decreasing the doping, N_d , of an n-drift region increases the on-resistance, as given by

$$R_{on-sp} = \frac{L_N}{e\mu_n N_d},$$

where e is the electronic charge and μ_n is the electron mobility. The relationship between on-resistance and blocking voltage is given by

$$R_{on-sp} = \frac{4V_B^2}{\varepsilon_s \mu_n E_c^3},$$

where ε_s is the dielectric constant of the semiconductor and E_c is the critical electric field.

The simplest method to break this design tradeoff is to move to a semiconductor material with a higher critical electric field.⁶ Ultrawide bandgap (UWB) materials have critical fields >2 MV/cm and enable the use of thinner, more highly doped voltage-blocking

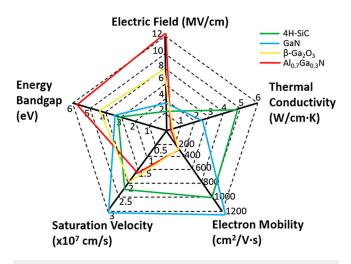


FIG. 1. Comparison of 4H-SiC, GaN, β-Ga₂O₃, and Al_{0.7}Ga_{0.3}N: critical electric field, thermal conductivity, electron mobility, saturation velocity, and energy bandgap.

layers, which can reduce on-resistance by an order of magnitude relative to equivalent Si devices. Figure 1 shows a pentagram diagram comparing some of the key properties related to device operation (critical electric field, thermal conductivity, electron mobility, saturation velocity, and energy bandgap) for the current wide bandgap (4H-SiC, GaN) and most developed ultrawide bandgap (β -Ga₂O₃ and Al_{0.7}Ga_{0.3}N) semiconductors.

The critical electric field scales as a power of the bandgap. The exponent has generally been reported to be between 1.8 and 2.5, depending on the model used. A recent re-examination of data from a variety of semiconductors concluded that $E_c \sim E_g^{-18}$ but noted the critical field is dependent on temperature and doping. Figure 2 shows approximately how the critical electric field scales with bandgap using the Hudgins model for SiC and various compositions of AlGaN. High breakdown electric field and low conduction losses mean that ultrawide bandgap devices can achieve the same blocking voltage and on-state resistance with a smaller form factor. There is interest in extending the performance limits using ultrawide bandgap materials that could potentially outperform SiC/GaN technology for power switching and power amplifier applications, since the on-state resistance at a given voltage decreases with bandgap.

There is interest in a number of less developed semiconductors with bandgaps larger than GaN or SiC^{16-34} for power switching and power amplifier applications. This is an area of rapid progress as new applications such as electric vehicles and lightweight systems like drones emerge. These materials include diamond, $^{17-22}$ boron nitride (BN), 23 high Al-AlGaN, $^{10,24-28}$ and $\mathrm{Ga_2O_3}$. $^{29-34}$ The high Al-AlGaN technology looks highly suited to lateral power devices but lack of large area, cheap native substrates, and issues with vertical conductivity may limit its use in vertical power devices. $^{24-28}$ The advantages of AlGaN are its ultrawide bandgap and corresponding high critical breakdown field, E_c . As discussed earlier, since E_c has been proposed to scale as a power law with bandgap $E_c \sim E_g^{2.5}$, then moving to high aluminum

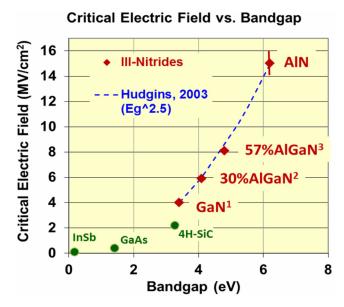


FIG. 2. Estimated critical electric field as a function of bandgap for 4H-SiC and various compositions of $Al_xGa_{1-x}N$. Adapted from Kaplar *et al.*, "A new analysis of the dependence of critical electrical field on semiconductor bandgap," paper presented at the 236th Electrochemical Society Meeting, Atlanta, GA, October 2019 and based on the model of Hudgins *et al.*, IEEE Trans. Power Electron. **18**, 907 (2003).

content (70% and above) pushes the bandgap to ≥5.7 eV and the theoretical breakdown to 13.4 MV/cm in accordance with the power law. Drawbacks include a lower electron mobility than binary alloys and difficulty in doping high Al-AlGaN. The usual Si dopant ionization level becomes very deep in Al-rich AlGaN, and ion implantation activation efficiency is low. While the initial device performance on these so-called UWB semiconductors looks promising, many challenges exist, including growth maturity, thermal limits, cost, and reliability in these material systems. Another of these materials is Ga_2O_3 , particularly the β-polymorph, which is the most stable and most studied one. Polymorph of Ga_2O_3 has a large power figure-of-merit and is available in large area bulk and epitaxial layer form.

Commercially available SiC and GaN power devices still have high cost and limited availability of the native substrates compared to Si. SiC and GaN cannot be grown from the melt like Si and the commercialized techniques for their growth, such as seeded sublimation (also known as physical vapor transport, ammonothermal and hydride vapor phase epitaxy), only produce relatively high cost substrates in limited sizes. By contrast, for Ga₂O₃, the bulk growth methods of Czochralski, float-zone, edge-defined film-fed growth, and vertical Bridgman methods all produce low cost, large crystals. ²⁹⁻³¹ Ga₂O₃ is best suited to high-voltage applications because of its large bandgap. Combining Ga₂O₃ with In₂O₃ or Al₂O₃ allows tuning of the atomic and electronic structure.

The other ultrawide bandgap semiconductors under development are diamond and BN. These suffer from high cost (diamond) and lack of mature technology (BN), but continue to be investigated because of their favorable materials properties.

There is generally an absence of good wet etching options for most of the wide bandgap semiconductors, with molten KOH the only universal option. However, this is impractical for pattern transfer and can only be used for blanket etching. The strong bonding of these materials always requires a high ion-driven (physical) component to the plasma etching, where it is generally understood that without ion-driven reactions, the etch rates under low ion density/energy conditions are very low. The challenge is to achieve anisotropic pattern transfer without excessive mask degradation under ion-driven etch conditions. It is also common to observe different etch rates depending on whether the plasma conditions are "neutral-starved" (reactive neutrals or radical density low) or "ion starved" (limited ion-enhancement due to low ion density) regimes. There is usually a threshold ion energy for the initiation of etching, since the etch products often do not have sufficient volatility to leave the surface without assistance by the physical sputtering from the impinging ions. The etch rate is generally proportional to the square root of the ion energy.

In this review, which is dedicated to the memory of John Coburn, we will discuss the plasma chemistries, masking requirements, and current state-of-the art in plasma etching of wide bandgap and ultrawide bandgap semiconductors.

II. DRY ETCHING OF WIDE AND ULTRAWIDE BANDGAP SEMICONDUCTORS

The predominant advantage of dry over wet etching is anisotropy. $^{35-47}$ Almost all semiconductors except Si in some plasma chemistries are etched by the ion-enhanced energetic mechanism. 42 In the case of Si, there can be surface reactions with neutral radicals (such as passivation of Si by chlorine to make $SiCl_x$) but there is little etching until energetic ions enhance the reactivity of a substrate or product layer, allowing the formation of the volatile product. $^{40-47}$ The particular advantage of this etching mode is anisotropy. The basic configuration for the etch reactors employed is a parallel plate, planar plasma etching geometry, referred to as reactive ion etching (RIE). There is a zone, referred to as the plasma sheath that separates the plasma from the sample electrode. The electrodes become negatively charged by the electrons from the plasma because the electrons have greater mobility than do positive ions in the plasma.

A convenient yardstick for determining the importance of the need for a strong physical component of the etching mechanisms is the cohesive energy per bond of the semiconductor. These are listed in Table I, along with that of Si. For low energy ions (<100 eV), there may be significant differences in the resultant physical sputtering rates; however, for high energies (e.g., 500 eV ion), the difference is not great. However, the requirement for minimizing mask degradation and also ion-induced damage in the semiconductor means that practical etch processes always use relatively low ion energies.

The use of high-density plasma etch systems including electron cyclotron resonance (ECR), inductively coupled plasma (ICP), and magnetron RIE, has resulted in improved etch characteristics for the wide bandgap and ultrawide bandgap semiconductors as compared to RIE. 48-65 This is attributed to plasma densities

TABLE I. Bond energies for wide bandgap semiconductors along with Si for comparison.

| Material | Cohesive energy per bond (eV) | |
|-------------|-------------------------------|--|
| Si | 1.49 | |
| GaN | 2.24 | |
| AlN | 2.88 | |
| Diamond | 3.46 | |
| BN | 3.24 | |
| SiC | 2.57 | |
| $B-Ga_2O_3$ | 2.34 | |

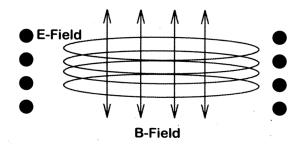
 $(10^{11}-10^{12}~{\rm cm}^{-3})$, which are typically 2 orders of magnitude higher than RIE $(10^9-10^{10}~{\rm cm}^{-3})$, thus improving the bond-breaking efficiency in these strongly bonded semiconductors and the sputter desorption of etch products formed on the surface. The magnitude of the ion flux to the wafer determines the rate of reaction. It does not determine the efficiency of reaction. The efficiency of reaction is more a function of ion energy. You can have the same ion energy in RIE and ECR, depending on the substrate bias. So the efficiency can be the same while the rate is different.

Additionally, since ion energy and ion density can be more effectively decoupled as compared to RIE, plasma-induced damage is more readily controlled. High-density ECR plasmas are formed at low pressures with low plasma potentials and ion energies due to magnetic confinement of electrons in the source region. The sample is located downstream from the source to minimize exposure to the plasma and to reduce the physical component of the etch mechanism. Anisotropic etching can be achieved by superimposing an rf bias (13.56 MHz) on the sample and operating at low pressure (<5 mTorr) to minimize ion scattering and lateral etching. However, as the rf biasing is increased, the potential for damage to the surface increases.

ICP offers another high-density plasma etch platform to ern group-III nitrides. 51–58,65 ICP plasmas are formed in a pattern group-III nitrides.⁵¹ dielectric vessel encircled by an inductive coil into which rf power is applied. The alternating electric field between the coils induces a strong alternating magnetic field. In a well operating ICP, the electromagnetic skin depth is a few centimeters, so there is essentially no magnetic field on axis due to efficient absorption by the plasma of the electromagnetic wave launched by the antenna. The plasma is peaked on axis because the transport is diffusion dominated not because of magnetic fields. The plasma density is high because of the high efficiency of heating electrons in the inductive field.⁶ Since ion energy and plasma density can be effectively decoupled, uniform density and energy distributions are transferred to the sample while keeping ion and electron energy low. Thus, ICP etching can produce low damage while maintaining fast etch rates. Anisotropy is achieved by superimposing of rf bias on the sample. A schematic of a typical ICP chamber and the configuration of the electric and magnetic fields is shown in Fig. 3.

A comment can be made concerning the blanket etch uniformity for these technologies—given the fact that the wide bandgap and ultrawide bandgap wafers are small compared to Si (generally

Inductively Coupled Plasma



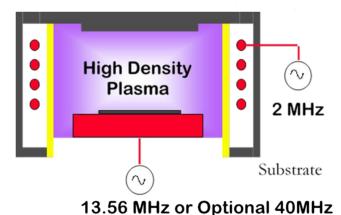


FIG. 3. Schematic of the configuration of a typical ICP reactor showing the direction of the magnetic field and the geometry of the chamber.

4 in. diameter or less for GaN and SiC and 2–3 in. diameter for the ultrawide bandgap wafers), the etch uniformities are excellent, with variations in etch depth over the entire wafer of <5%.

III. PLASMA CHEMISTRIES

A. Wide bandgap

The three basic members of the III-nitride family are GaN, InN, and AlN, and alloys of these are used in devices, e.g., AlGaN and InAlN. Microfabrication of vertical power devices in this materials system often involves a mesa isolation step to define the body of the device and provide electrical isolation from near neighbors. In light-emitting diodes, the uppermost p-layer must be removed by dry etching to expose the underlying n-side of the pn junction for contacting. Since most of the III-nitrides have a high resistance to wet chemical etching, mesa isolation is typically accomplished using chlorine-based plasma etching, which is the preferred technique for patterning GaN and the related members of the nitride family. 48,65 Minimization of dry etch damage and sidewall roughness is critical as rough and damaged etched surfaces contribute to increased leakage currents and overall device degradation. Previous

investigations have shown that Cl_2/Ar plasmas can yield lower surface roughness and damage when compared to plasmas with BCl_3 additives, although the latter is commonly added because of its ability to remove oxides that inhibit the initiation of etching. 48,65 The etch products for GaN are typically $GaCl_3$ and N_2 , meaning that the chlorine radical density and the ion density are key factors, since a limiting step is the bond-breaking to allow these etch products to form. An alternative plasma chemistry to the chlorine-based ones is $CH_4/H_2/Ar$, which produces much slower etch rates and may have issues with polymer formation distorting the pattern transfer, but has fewer safety issues than chlorine and is well-suited to university labs.

Figure 4 summarizes a comparison of typical etch rates for GaN in Cl₂/Ar discharges in different types of reactors. ^{48,49} What is clear is that the high ion density variants (ICP and ECR) produce much higher etch rates than their low ion density counterparts (RIE and reactive ion beam etching, RIBE). The other obvious trend is the strong dependence of etch rate on dc self-bias, corresponding to the energy of the incident ions, which enhance the production and removal of volatile etch products. ^{59–70} This ionenhanced etch mechanism, ^{71–76} relying on a synergism between the chemical and physical etch components, was pioneered by Coburn and co-workers. ^{35–47}

Figure 5 shows a typical dependence of GaN etch rate on dc selfbias under ICP conditions in the common Cl₂/BCl₃/Ar plasma chemistry, which is a versatile and commonly applicable one for nitrides, since it combines the Lewis acid BCl₃, which is an effective remover of native oxides and tends to produce more consistent results.^{48,65}

Harrison *et al.*⁷¹ demonstrated ultradeep ($\geq 5 \, \mu m$) ECR plasma etching of rows of GaN micropillars. Parametric studies on the influence of the applied rf power, Cl₂ content in a Cl₂/Ar plasma chemistry, and operating pressure on the etch depth, GaN-to-SiO₂

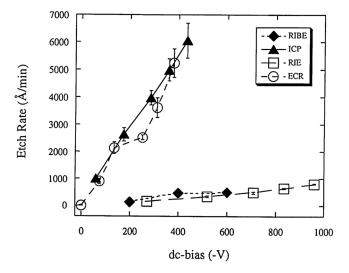


FIG. 4. Comparison of GaN etch rates in Cl_2/Ar plasmas for different etch techniques—ICP, ECR, RIE, and RIBE. The two high ion density methods produce the fastest etch rates.

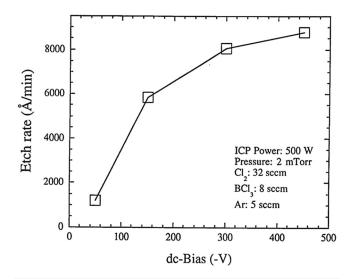
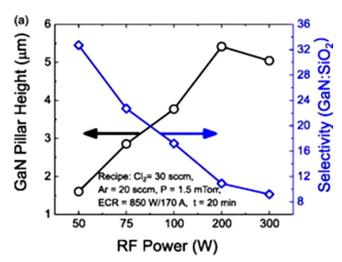


FIG. 5. GaN etch rate in Cl₂/BCl₃/Ar ICP discharge as a function of dc self-bias. The latter correlates to the incident ion energy and shows that the etching is ion-assisted.

selectivity, and surface morphology were performed. Figure 6 shows the variation in the micropillar etch height and selectivity plotted as a function of rf power. The Etch depths were generally found to increase as the rf power or ion energy was increased due to the enhancement of the physical sputtering component of the etch plasma. High rf powers can promote faster etch rates from increased ion bombardment, which improves the Ga-N bondbreaking and sputter-assisted desorption of etch by-products. From 50 to 200 W, the etch rates between \sim 0.08 and 0.27 μ m/min. Etch depths of >10 μ m were achieved over a wide range of parameters. Etch rates and sidewall roughness were found to be most sensitive to variations in rf power and % $\rm Cl_2$ in the etch plasma. Selectivities of >20:1 $\rm GaN:SiO_2$ were achieved under several chemically driven etch conditions where a maximum selectivity of \sim 39:1 was obtained using a pure $\rm Cl_2$ plasma. $\rm ^{63}$

The etch rates, surface morphology, and sidewall profiles of features formed in GaN/InGaN/AlGaN multiple quantum wells by Cl₂-based dry etching are a good indicator of how the change in composition alters etch rates and selectivity. The chlorine provides an enhancement in etch rate of over a factor of 40 relative to the physical etching provided by Ar and the etching is reactant-limited (also called neutral-starved) until chlorine gas flow rates of at least 50 standard cubic centimeters per minute (power 250 W, volume residence time approximately 30 s, and fractional dissociation 5%). Mesa sidewall profile angle control is possible using a combination of Cl₂/Ar plasma chemistry and SiO₂ mask. N-face GaN is found to etch faster than Ga-face surfaces under the same conditions.

Damage effects during etching of these structures alters the electrical characteristics of these InGaN multiquantum-well pn junctions. 56,57 Structures of this type were exposed to either Ar or $\rm H_2$ inductively coupled plasmas as a function of both rf chuck power (controlling incident ion energy) and source power



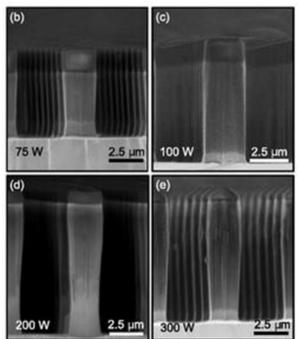


FIG. 6. (a) Micropillar etch height and GaN:SiO $_2$ etch selectivity as a function of rf power for 20 min of etching. The lower panel shows SEM images of lithographically defined hexagonal micropillars with $2\,\mu\rm m$ diameters etched for 20 min using (b) 75 W (Vdc = -11 V), (c) 100 W (Vdc = -35 V), (d) 200 W (Vdc = -72 V), and (e) 300 W (Vdc = -128 V) rf power. Reprinted with permission from Harrison *et al.*, J. Vac. Sci. Technol. A **35**, 061303 (2017). Copyright 2017, American Vacuum Society.

(controlling ion flux), to simulate the ion bombardment effects that occur during etching. The forward turn-on voltage was increased by both types of plasma exposure and was a function of both the incident ion energy and flux. The reverse bias current in the pn junctions was much larger in the case of H₂ plasma exposure,

indicating that the preferential loss of nitrogen leads to increased surface leakage. The current transport in the junctions was dominated by generation-recombination (ideality factor \sim 2) both before and after the plasma exposures.

Atomic layer etching of GaN has been reported by Kauppinen et al. 22 using sequential surface modification by Cl₂ adsorption and removal of the modified surface layer by low energy Ar plasma exposure in an RIE system. It was possible to use a simple photoresist mask for patterning GaN(0001) films by Atomic Layer Etching (ALE). The etch rate was constant with the number of ALE cycles, and the etch rate saturated when increasing the Ar ion dose. Almost exactly 1 ML etch per cycle was achieved with longer purge times.

Selective etching in the nitride family is possible, especially for GaN over AlGaN, because of the higher bond strengths of the latter. $^{48,63,65,78-81}$ Commonly used plasma chemistries include chlorine and oxygen gasses, with selectivity a strong function of oxygen ratio and also by RIE power, which controls ion energy. Maximum selectivity between GaN and Al $_{0.25}$ Ga $_{0.75}$ N achieved was at least 68.5 to 1. Optimum selectivity was obtained with a low oxygen flow to inhibit Al $_{0.25}$ Ga $_{0.75}$ N action while steadily etching GaN. Although Al $_{0.25}$ Ga $_{0.75}$ N acts as an etch stop with excellent selectivity, significant overetching can still cause damage to the underlying layers through ion bombardment.

B. SiC

Since the Si–C bond energy is very high, many plasma etching processes for SiC are very slow. $^{82-105}$ To achieve reasonable etch rates, high ion energies and a high plasma density are necessary. One application requiring deep etching is the fabrication of through-wafer via holes, which should have no trenching or micromasking and have excellent electrical connection after subsequent metal plating. This was developed across full wafers for use in AlGaN/GaN high electron mobility transistors (HEMTs) grown on SiC substrates for obtaining high thermal conductivity and microwave monolithic integrated circuits using ICP conditions. The key process parameters were found to be choice of wafer platen, hard mask, gas chemistry, surface treatments, and plasma parameters in order to achieve an acceptable etch rate while simultaneously minimizing trenching (where scattering of ions off the sidewall leads to grooves at the base of the sidewall) and micromasking (where involatile fragments of the mask or polymer from the plasma accumulate on the surface and prevent etching of the semiconductor underneath those regions) that can harm via yield. In addition, the issue of wafer thickness variation and etch nonuniformity leading to punch through of Au pads at the bottom of the vias was addressed by the addition of a metal layer to the front side of the wafer. An extreme example of micromasking is shown in Fig. 7, where an SiC wafer was etched in an SF₆/O₂ discharge under nonoptimized conditions.

An acceptable SiC etch process for through-wafer via formation will simultaneously realize etch rates in excess of 2500 Å/min, highly anisotropic profiles, smooth surface morphologies with minimal micromasking, and minimal trenching. So far, a variety of plasma chemistries based on fluorine, including NF₃, NF₃/O₂, SF₆/O₂, SF₆/O₂, SF₆/O₂/Ar, have been used for SiC etching. Si-105 The active etch species are then the F radicals, forming SiF_x and CF_x etch products once the bonds are broken by ion bombardments. The

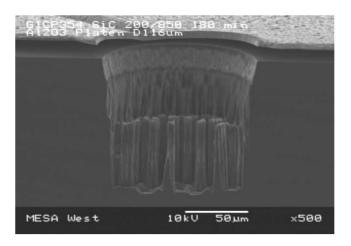


FIG. 7. Via etched into SiC using an ICP $\rm SF_6/O_2$ discharge at 7 mTorr, an ICP power of 900 W, and an rf power of 250 W.

spontaneous etch rates without ion bombardment are very low. While rates as high as 8000 Å/min have been reported in some chemistries, the profiles often show significant trenching and micromasking. This micromasking effect can be reduced by introducing an Ar pretreatment, as well as with the introduction of CF₄ to SF₆/He plasma chemistry.⁹⁷ The proper selection of mask and platen (i.e., the electrode on which the sample sits during etching) material is also critical. The use of fluorine-based plasmas is appropriate to form volatile etch products with SiC, which are believed to be SiF4 and CF₂. In addition, the use of O₂ in the plasma may provide an additional volatilization path for the C in the form of CO, CO₂, or COF₂ while simultaneously increasing the density of F free radicals. The choice of mask and platen are important, since they are potential sources of nonvolatile etch products. Aluminum containing platens such as sapphire, aluminum nitride, anodized aluminum, and nickelplated aluminum all create unacceptable levels of micromasking in the vias since they lead to particulate formation on the wafer. Use of silica and silicon is also inappropriate due to the rapid etch rate of these materials in F-based plasma. The solution settled upon was the use of graphite.

For a practical SiC via process, etch rates of >2500 Å/min are a reasonable target since the via depth of $\geq 100 \,\mu m$ would correspond to a 400 min etch time. This etch time may even be longer, due to the presence of RIE-lag, i.e., where the rate slows significantly as via depth increases, due to increasing difficulty transporting the reactive gases to and volatile products from the SiC surface. Figure 8 shows results for via etching over time for SF₆/O₂, and either 750/100 W or 950/250 W ICP/rf power. While the 950/250 W powers display much higher etch rates, the plasma is not stable at these elevated powers for long periods because this power is close to the maximum available on this particular etch system. Thus, it is necessary to use the slower 750/100 W etch parameters. This problem could be alleviated with use of a higher power level rf supply. At these powers, an initial etch rate of ~3500 Å/min at 100 min is achieved. The etch rate decreases to 3000 Å/min at 350 min. The addition of He or Ar into the SF_6/O_2

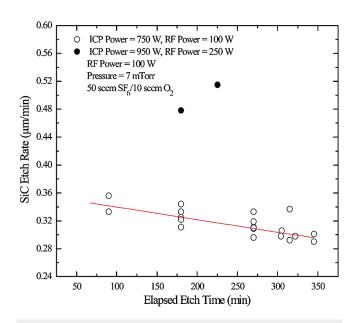
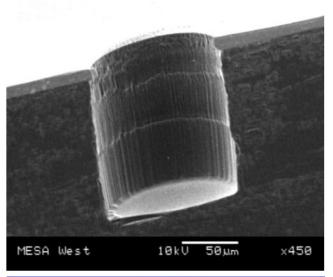


FIG. 8. SiC etch rate for ICP discharges of SF_6/O_2 at 7 mTorr, as a function of ICP and rf power and etch time. Reprinted with permission from Voss *et al.*, J. Vac. Sci. Technol. B **26**, 487 (2008). Copyright 2008, American Vacuum Society.

plasma was found to reduce the amount of micromasking. $^{87-90,97}$ These additional gases serve the dual purpose of significantly increasing the SiC etch rate without adversely affecting the sidewall profile or Ni mask etch rate. In addition, an Ar cleaning step of the SiC surface prior to etching dramatically decreased the amount of pillar formation. The SF₆/O₂/Ar discharges allow the use of Al-containing or graphite platens without an increase in the amount of micromasking in the vias. Etch rates of almost 5000 Å/min were achieved with minimal micromasking and trenching at the bottom of the via holes. Figure 9 shows SEM micrographs of deep features etched into SiC using this optimized process. The profile has the required ~86° sloped profile sidewalls and the walls are smooth. The uniformity across the wafer was $\pm 2.2\%$.

In terms of masking, SiO_2 is a common choice for shallow SiC etching, using SF_6/O_2 plasma chemistry. This relatively clean chemistry produces etching species with good volatility. Selectivity to SiO_2 is 2.1:1, highlighting the issue of mask selectivity. These levels of selectivity call for a relatively thick mask or suggest the use of alternative conditions and/or masking materials. The selectivity to Al is more than double the selectivity to SiO_2 —around 5:1—and the quality of the resulting etches is directly comparable. Smooth sloped 87–88° profiles with no microtrenches and an aspect ratio of more than 5:1 can be produced using Ni masks at an etch rate of around 250 nm/min. 97,102 Uniformity is <±5% across a 100 mm wafer. SU8 is another widely used epoxy-based photoresist. The selectivity of SU8 to the SiC etching process was low (0.55:1).

Voss et al. 102 also reported using SF₆ reactive ion etching to produce nanotexturing of SiC. The effect of etching process power and time was optimized to demonstrate sub-1% specular reflectance and below 5% total reflectance over the 400–2000 nm spectral range.



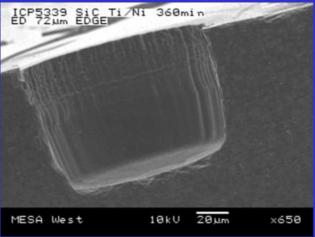


FIG. 9. SEM images of deep features etched into SiC using ICP SF_8/O_2 discharges and an Ni/Ti mask. Reprinted with permission from Voss *et al.*, J. Vac. Sci. Technol. B **26**, 487 (2008). Copyright 2008, American Vacuum Society.

Recent advances in etching SiC have included accelerated etch rates using femtosecond laser modification, ¹⁰³ damage-free finishing of chemical vapor deposition (CVD) SiC by a combination of plasma etching and plasma-assisted polishing and the revealing of dislocations using ICP etching. ^{104,105}

IV. ULTRAWIDE BANDGAP

A. High Al-AlGaN

High Al-content AlGaN has the potential to enable the next generation of power switching transistors, using its ultrawide bandgap to increase the output power and breakdown voltage relative to GaN and SiC. $^{106-123}$ The bandgap of this system varies from $4.4\,\mathrm{eV}$ for GaN to $6.2\,\mathrm{eV}$ for AlN. A common device structure

for this materials system is a high electron mobility transistor (HEMT), in which a 2-dimensional electron gas channel is formed at the interface between two layers of differing bandgap, e.g., an Al_xGa_{1-x}N channel layer may be used in conjunction with a wider-bandgap $Al_yGa_{1-y}N$ barrier layer. A typical device structure is shown in Fig. 10. 113 To date, numerous groups have demonstrated high Al-content AlGaN/AlGaN HEMTs with good gate control and leakage current. Devices containing pure AlN barriers are limited at present by source/drain Ohmic contact resistance. 10,122 Dry etching is employed for several steps of Al_yGa_{1-y}N/Al_xGa_{1-x}N HEMT fabrication, including mesa isolation and recessed gate structures. For example, in an AlN/Al_{0.85}Ga_{0.15}N barrier/channel heterostructure, source and drain contacts are fabricated by employing a dry etch of the AlN barrier and then regrowth of n+ GaN or ion implantation of Si. Douglas et al. 123 reported a comparison of the dry etching characteristics of Al_{0.71}Ga_{0.29}N, Al_{0.85}Ga_{0.15}N, and AlN, all of which were grown on an AlN buffer layer on a 1.3 mm thick sapphire substrate. An etch chemistry of Cl₂/BCl₃/Ar was employed, under ICP conditions.

Figure 11 shows the etch rate for the three materials as a function of either bias power (top) or plasma composition (bottom). There are several key points from these data. The first is that the etch rates increase with bias power, suggesting that the etching is ion-driven and is proportional to incident ion energy. The desorption of AlCl_x compounds from the surface is likely the rate-limiting step. The nonlinear trend in etch rate may result from an adsorption limited etch regime, in which reactive species are sputtered off the surface prior to desorbing from the surface as a volatile component. 123 The second point is that as the percentage of BCl₃ increases above 10%, the etch rate for all three compositions behaves very similarly with the overall etch rate decreasing and approaching zero at 100% BCl₃. In this case, BCl₃ does not produce a high concentration of Cl radicals, especially at low ICP and bias powers. This was confirmed by optical emission spectroscopy. The surface morphology for the three compositions of Al_xGa_{1-x}N was excellent surface morphology as long as a minimum of 30% BCl₃ was used in the plasma chemistry.

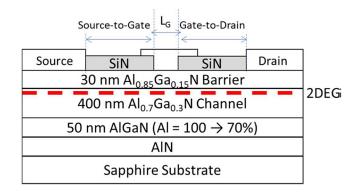


FIG. 10. Cross-sectional schematic of the typical high-Al HEMT structure. Reprinted with permission from Klein *et al.*, J. Electron. Mater. **48**, 5581 (2019). Copyright 2019, Springer.

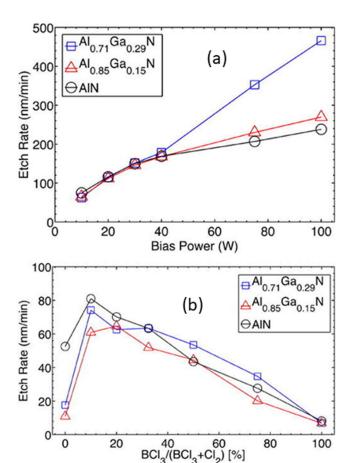


FIG. 11. Effect of bias power (a) on the BCl $_3$ content and (b) on the etch rate for Al $_{0.71}$ Ga $_{0.29}$ N, Al $_{0.85}$ Ga $_{0.15}$ N, and AlN in ICP discharges of Cl $_2$ /BCl $_3$ /Ar. The pressure was 3 mTorr, ICP power was 125 W, and gas flow was at 20% BCl $_3$ plus 5 SCCM Ar at 45 SCCM total gas flow, and bias (10 W) was held constant for the BC $_3$ content experiment. Reprinted with permission from Douglas *et al.*, J. Vac. Sci. Technol. A **35**, 021305 (2017). Copyright 2017, American Vacuum Society.

These results confirm that the $\text{Cl}_2/\text{BCl}_3/\text{Ar}$ chemistry can produce practical etch rates and good surface morphology for $\text{Al}_y\text{Ga}_{1-y}\text{N}/\text{Al}_x\text{Ga}_{1-x}\text{N}$ device fabrication. This is a common mixture to use under ICP conditions, with the BCl $_3$ removing aluminum oxide surface layers, the Cl_2 providing the chemical etch component and the Ar the physical component. It also has an acceptable selectivity to common mask materials. It is worth noting that the CH $_4/\text{H}_2/\text{Ar}$ plasma chemistry produces extremely slow etch rates for high Al-content AlGaN and is not widely used.

B. BN

BN is an emerging wide bandgap semiconductor for power electronics and deep UV photonic device applications, with an energy bandgap of $E_g \sim 5.9 \, \text{eV}$ for the hexagonal phase and 6.4 eV

for the cubic phase. 124-139 It also has potential applications in solid-state neutron detection through the fission reaction between the neutrons and ¹⁰B atoms. ^{130,131} BN is isoelectronic with carbon (diamond) and can possess sp2- and sp3-bonded phases. The four primary crystalline BN phases are the most common phase, hexagonal BN (h-BN), as well as rhombohedral BN, cubic BN (c-BN), and wurtzite BN. h-BN is comprised of sp2 B-N bonds forming planar hexagonal networks stacked along the c-axis in an AA'AA'A configuration. 128,132 The in-plane and c-axis lattice constants of h-BN are 0.250 and 0.666 nm, respectively, close to the values of graphite. c-BN has a zincblende lattice. As is the case for diamond, c-BN is an sp³-bonded phase, which exhibits very high values of thermal conductivity, hardness, strength, and radiation resistance. BAIN alloys have been suggested as heterobarriers for BAIN/AIN structures, and as quantum barriers for AlN quantum wells, potentially enabling optoelectronics in the ultradeep-UV range.

Hexagonal BN may be synthesized by CVD employing boron trichloride, ammonia, and hydrogen precursors. ¹³² Interestingly, the reverse reaction can be used for etching, i.e., the use of BCl₃/H₂ plasma chemistry will form volatile etch products under ion-assisted conditions where the physical component is used to break bonds. h-BN is stable in air up to 1000 °C, under vacuum up to 1400 °C, and in an inert atmosphere to 2800 °C. ¹³² An advantage of h-BN is its compatibility with AlGaN, and because of its good p-type doping capability and favorable band alignment, h-BN has advantages over AlN as a material for electron-blocking and hole-injector layers in AlGaN-based optoelectronics. ¹³²,133

There have been a number of plasma chemistries reported for dry etching of BN, 140 including CH₄/Ar at a rate of ~3 nm min⁻¹, Cl₂/Ar, and Ar/H₂, selective vapor phase etching of h-BN over c-BN using NH₃ or HCl at >600 °C, CF₄ /H₂, SF₆, and Cl₂/BCl₃/Ar under ICP conditions reaching 1.25 μ m min⁻¹. The etch products are expected to be BCl_x and N₂ with chlorine-based chemistries and BF_x and NF₃ and N₂ in fluorine-based discharges. The dependence of etch rate on ICP power at fixed rf power and pressure are shown in Fig. 12—the rates and anisotropy are clearly acceptable for device fabrication schemes. 140 At very high ICP powers, the dc self-bias on the sample decreases and this reduces the efficiency of etch product removal and also the initial bond-breaking that allows the etch products to form.

C. Ga₂O₃

 Ga_2O_3 is emerging as a viable candidate for power electronics, solar blind UV photodetectors, solar cells, and sensors with capabilities beyond existing technologies due to its large bandgap of $\sim 4.8~{\rm eV}.^{141-159}$ The theoretical breakdown field is $\sim 8~{\rm MV/cm}$, and the electron saturation velocity is $\sim 10^7~{\rm cm/s}$. Combined with availability of excellent crystalline quality native substrates grown by the standard melt-growth methods, high quality epitaxial films, and existence of wide bandgap ternaries of $(Al_xGa_{1-x})_2O_3$ that can be used as barrier layers in modulation doped field effect transistors, there is a basis for optimism about the technological prospects. 29,30,146,147 Donor doping is available in bulk and epi growth methods, and it is possible to grow semi-insulating buffer layers for lateral transistors. There is a need to pattern Ga_2O_3 when fabricating UV solar blind photodetectors and transistors. In the specific case of vertical

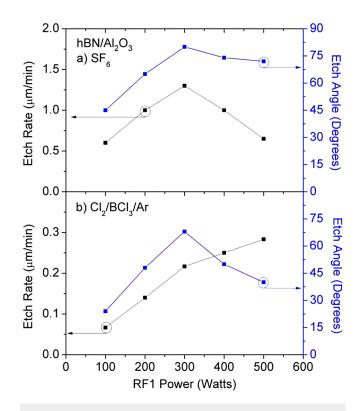
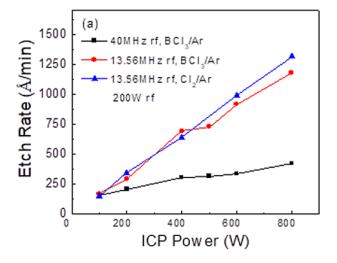


FIG. 12. ICP power dependence for h-BN epilayers etched in (a) SF $_6$ and (b) Cl $_2$ /BCl $_3$ /Ar discharges at 6 mTorr and 150 W rf chuck power. Reprinted with permission from Grenadier *et al.*, J. Vac. Sci. Technol. A **31**, 061517 (2013). Copyright 2013, American Vacuum Society.

rectifiers, no direct etching of the Ga_2O_3 is needed, but the rectifying contact and dielectric overlap (field plate) structures are defined by lithography and etching. The maximum etch rates reported to date are <150 nm min⁻¹. The plasma chemistries tried have included Cl_2/BCl_3 , Cl_2/Ar , BCl_3 , BCl_3/SF_6 , BCl_3/Ar , SF_6/Ar , and CF_4/O_2 . The highest rates are achieved under high-density plasma conditions, such as ICP. For thinning of exfoliated flakes, SF_6 provides a low and controllable rate. The plasma chemistry produces impractically slow etch rates.

The high bond strength of β -Ga₂O₃ suggests that ion-assisted etching will be the likely mechanism needed to achieve practical etch rates. ¹⁵³ This can be enhanced using Ar added to the chlorinated gases. The energy of ions striking the Ga₂O₃ is basically determined by the dc self-bias on the sample electrode. This self-bias was –102 to –820 V for our set of conditions for Cl₂/Ar plasma etching. Figure 13(a) shows the Ga₂O₃ etch rate as a function of ICP source power for different conditions of frequency of power applied to the sample electrode (13.56 or 40 MHz) and the BCl₃/Ar or Cl₂/Ar plasma chemistries. The corresponding dc self-biases are shown in Fig. 13(b). Note that as the ICP source power is increased, the dc self-bias on the sample position is suppressed because of the higher ion density in the plasma. The etch rate increases monotonically with ICP source power, due to the increase



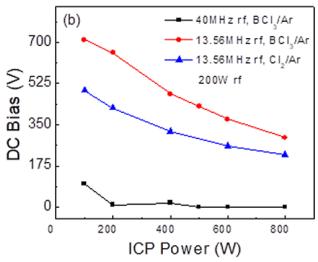


FIG. 13. (a) β-Ga₂O₃ etch rate as a function of ICP power at constant 200 W rf power (13.56 MHz) for three different chamber configurations and either BCl₃/Ar or Cl₂/Ar plasma chemistries. (b) dc self-biases under these conditions. Reprinted with permission from Yang et al., J. Vac. Sci. Technol. B 35, 031205 (2017). Copyright 2017, American Vacuum Society.

in source power producing higher ion and reactive neutral densities. The highest etch rate achieved was ~1300 Å min⁻¹ using 800 W ICP source power and 200 W chuck power (13.56 MHz) with both plasma chemistries. Shah and Bhattacharya reported a selectivity of 2.7 over an SiN_x mask under similar conditions.

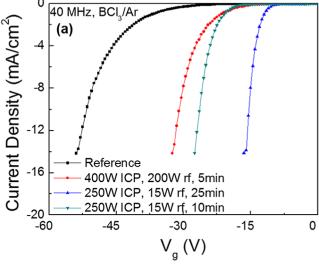
Under some conditions, the BCl₃-based discharges produce higher etch rates than Cl₂-based. A possible explanation is that BCl₃ radicals (probably BCl) react with the oxygen in the Ga₂O₃ and the BCl₂⁺ ions provide ion-enhanced sputtering, since it is a Lewis acid with a strong affinity for removing oxides. BCl3 is known to increase the Cl radical density when added to chlorinebased discharges at low concentrations but also the ion density increases due to B⁺ and BCl_x⁺ formation. In Cl₂-based discharges, the atomic and molecular chlorine species do not have the same ability to remove oxygen.1

There is a threshold ion energy for the initiation of etching of ~75 eV for pure Ar, from fitting to the standard model for ionassisted etching in a collision-cascade process.^{48,65} The etch rate will be proportional to $E^{0.5}-E_{TH}^{0.5},\,\mbox{where}\,\,E$ is the ion energy and E_{TH} is the threshold energy. Ion-assisted etching leads to anisotropic sidewalls because of the absence of the chemical etchings. which produces undercutting of patterned features.

Schottky barrier height measurements are a sensitive indicator of near-surface damage created by dry etching. 151 Figure 14 shows the reverse I-V characteristics of diodes fabricated on the etched surfaces using either 40 MHz (a) or 13.56 MHz (b) rf chuck biases with different ICP powers and etch times, all with BCl₃/Ar discharges. The reference diodes that were not exposed to the plasma exhibited reverse breakdown voltages of ~50 V. In plasma exposed diodes, the reverse breakdown voltage was significantly reduced as a result of ion-induced damage and nonstoichiometry of the surface. The extent of this degradation depended on the self-bias, which controls the incident ion energy and hence the density of point defects created by the impinging ions. The damage induced by plasma exposure could be essentially be completely removed annealing at 450 °C.

Minority carrier diffusion lengths were also measured from the exponential decay of electron beam-induced current (EBIC) as a function of distance from the gate. 156 The EBIC data showed only a small reduction in minority carrier diffusion length from $350\,\mu\mathrm{m}$ in the control sample to $307 \,\mu \mathrm{m}$ in the plasma exposed $\mathrm{Ga_2O_3}$. It is also possible to simulate the maximum damage created by subjecting samples to a pure Ar ICP treatment. This plasma exposure caused the built-in voltage of Ni Schottky diodes deposited on the plasma treated surfaces to decrease from 1 to -0.02 V due to the increased deep trap concentration in the near-surface region. There was an increase in the top ~200 nm of the plasma treated layer of the concentration of E2* (E_c -0.8 eV) and E3 (E_c -1.05 eV) deep electron traps. There was also an increase in the upper ~100 nm of the film in the concentration of deep acceptors with optical threshold for ionization of ~2.3 and 3.1 eV. Such defects at the surface led to a significant increase in reverse current, an increase in the ideality factor in forward current, and a dramatic decrease in the diffusion length of nonequilibrium charge carriers from 450 to 150 nm. Figure 15 summarizes the change in deep trap concentration in the near-surface region as a result of Ar plasma exposure.

Similarly, exposure of the Ga₂O₃ surface to hydrogen plasmas produced a $\sim 2.5 \,\mu$ m-thick surface region, depleted of electrons at room temperature. 158,159 This thickness correlates with the hydrogen penetration depth based on previous experiments. The Fermi level pinning position in the H treated film was E_c -1.05 eV. Annealing at 450 °C decreased the thickness of the depletion layer to $1.3 \,\mu m$ at room temperature and moved the Fermi level pinning position to E_c-0.8 eV. Further annealing at 550 °C almost restored the starting shallow donor concentration and the spectra of deep traps dominated by E_c -0.8 eV and E_c -1.05 eV observed before hydrogen treatment. Hydrogen plasma exposure produces surface damage in the near-surface region and passivates or compensates shallow donors.



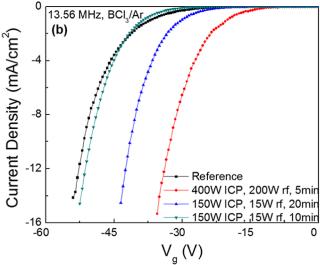


FIG. 14. Reverse I-V characteristics of diodes fabricated on the etched surfaces using either the 40 MHz (top) or 13.56 MHz (bottom) rf chuck biasing conditions with BCl₃/Ar discharges. Reprinted with permission from Yang *et al.*, J. Vac. Sci. Technol. B **35**, 051201 (2017). Copyright 2017, American Vacuum Society.

We can conclude from the published I-V, PL and EBIC data that ICP plasma creates small densities of point defects in the near-surface region of Ga_2O_3 . The O/Ga ratio in the etched region does not change from that in the reference material, at least to the sensitivity of Auger electron spectroscopy.

D. Diamond

The usual plasma chemistries for etching diamond are based on either $\rm O_2$ or $\rm H_2$. $^{160-185}$ In the latter case, the hydrogen selectively reveals defects, with steps, etch pits, subgrains, and collapse

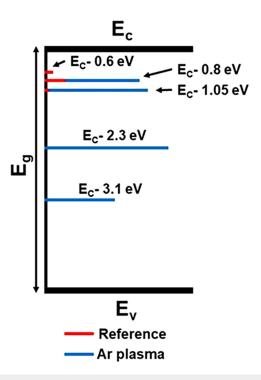
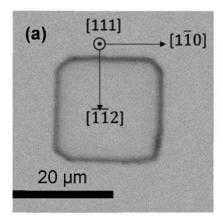
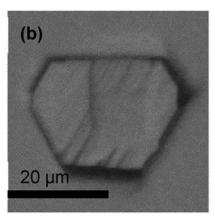


FIG. 15. Summary of energy levels and their concentration detected before and after Ar plasma exposure. The length of the bars represents the relative concentrations.

of crystal grains appearing on the diamond films during the etching process.¹⁸⁵ The different regions of diamond films are preferentially etched at different treatment stages by hydrogen plasma. The dielectric properties of nanocrystalline diamond can be improved by hydrogen plasma exposure, with dielectric constant close to intrinsic diamond and low dielectric loss. Kuroshima reported the formation of atomically flat diamond (111) surfaces by anisotropic etching during hydrogen plasma treatment, a result of the different binding energies of the surface atoms. Figure 16 shows images of diamond (111) surfaces before and after hydrogen plasma etching. The trench structure of the diamond (111) surface was observed to change from hexagonal to square shape after hydrogen plasma etching for 60 h [Fig. 14(b)]. As shown in Fig. 16(c), the trench structure changed from triangular to hexagonal shape by increasing the duration of the hydrogen plasma etching treatment to 100 h.

Pure oxygen plasmas have been used to produce diamond micromechanical components, while the majority of etching processes are based on oxygen plasma in combination with a secondary gas, such as Ar, Cl₂, CHF₃, or CF₄. The latter are used to reduce micromasking that originates in particles sputtered from the mask or surfaces of the etch chamber and balance the chemical etch component from the O_2 . The etch products are expected to be CO_x species. Figure 17 shows a deep set of features etched into single crystal diamond at a rate of $0.22 \, \mu \text{m min}^{-1}$ using an O_2/Ar





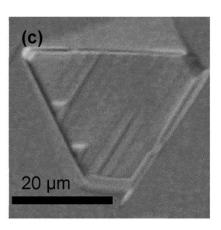


FIG. 16. Optical images of diamond (111) surfaces on a 10 × 10 µm² trench structure: (a) before H₂ plasma etching treatment; (b) after a 60 h H₂ plasma etch; and (c) after a 100 h H₂ plasma etch. Reprinted with permission from Kuroshima et al., Appl. Surf. Sci. 422, 452 (2017). Copyright 2017, Elsevier.

discharge. Under ICP conditions with 800 W source power, 100 W rf chuck power, 5 mTorr pressure, etch rates of polycrystalline diamond were in the range $0.8-3 \,\mu\mathrm{m \, min}^{-1}$. Muchnikov et al. 181 reported etch rates for single crystal diamond of $2\,\mu\mathrm{m~min}^{-1}$ at dc bias voltages of 240 V in Ar/Cl₂. Golovanov *et al.*¹⁸³ used SF₆ plasma chemistry to achieve etch rates of $5 \,\mu\mathrm{m}$ min⁻¹ at a dc bias of 250 V, with selectivities of 3 to Al₂O₃, 11 to Al, 75 to Ni, and 0.14 to Mo.

A key requirement for obtaining smooth surface morphologies is to first polish the diamond surface mechanically using the

etch rates, and selectivities to common mask materials is shown in Table II. Note that selectivities well above the target value of 10 are readily achievable.

standard diamond grit process. It is also possible to cycle etch con-

A summary of the plasma chemistries, maximum achievable

ditions to reduce the formation of micromasks.

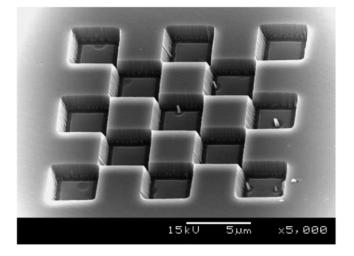


FIG. 17. SEM of smooth etched features in diamond after an Ar/O2 plasma etch. Reprinted with permission from Enlund et al., Carbon, 43, 1839 (2005). Copyright 2005, Elsevier.

TABLE II. Summary of common dry etch chemistries for wide bandgap and ultrawide bandgap semiconductors, achievable etch rates, and selectivity to common mask materials.

| | Max etch rate | | |
|---------------------------------------|---------------------------|------------------------------|--|
| Plasma chemistry | $(\mu \text{m min}^{-1})$ | Selectivity to mask | |
| GaN | | | |
| Cl ₂ /Ar | 0.9 | 30 to SiO ₂ | |
| Cl ₂ /BCl ₃ /Ar | 0.8 | 15 to Ni | |
| CH ₄ /H ₂ /Ar | 0.2 | 10 to SiO ₂ | |
| SiC | | | |
| SF_6/O_2 | 0.5 | 5 to Al | |
| NF ₃ /O ₂ | 0.4 | 3 to SiO ₂ | |
| $Al_{0.7}Ga_{0.3}N$ | | | |
| Cl ₂ /BCl ₃ /Ar | 0.5 | 5 to PR | |
| BN | | | |
| SF ₆ | 1.25 | 4-7 to Al | |
| Cl ₂ /BCl ₃ /Ar | 0.3 | 8-10 to Ni | |
| Ga_2O_3 | | | |
| BCl ₃ /Ar | 0.13 | $2.7 \text{ to } SiN_x$ | |
| Cl ₂ /Ar | 0.13 | 3 to SiN_x | |
| Diamond | | | |
| O ₂ /Ar | 2-13 | <1 to PR | |
| Cl ₂ /Ar | 6 | 5 to SiO ₂ | |
| SF_6/O_2 | 2-16 | 75 to Ni | |
| CF ₄ /O ₂ | 1-9 | 50 to Al | |
| H_2 | 0.09 | 10 ³ to any metal | |



V. SUMMARY AND CONCLUSIONS

A common theme with wide bandgap and ultrawide bandgap semiconductors is their generally high bond strengths. In terms of plasma etching, this translates to ion-assisted etch mechanisms. The advantage of operating in this mode is the anisotropy of the etched features, but a drawback is the increased attention that must be paid to mask selection. It is common to use metal or dielectric masks when etching deep features in these materials. Ohmic contacts are a significant challenge for these materials. In conventional semiconductors, Ohmic contacts are typically realized by metalsemiconductor junctions in which the potential barrier becomes very thin due to high doping in the semiconductor, leading to tunneling conduction. Achieving high levels of doping in wide bandgap semiconductors is problematic. An alternative way to ion implantation doping to get good Ohmic contacts in wide bandgap materials is the etch/regrowth approach. This requires low amounts of induced damage and changes to the stoichiometry of the surface that is processed and requires low damage etch conditions. Generally, the high-density plasma etching approaches, predominantly ICP, combined with polymer-free plasma chemistries, are employed for the semiconductors discussed here.

ACKNOWLEDGMENTS

This work at UF was sponsored by the Department of the Defense, Defense Threat Reduction Agency (No. HDTRA1-17-1-011), monitored by Jacob Calkins and also by the National Science Foundation (NSF) under No. DMR 1856662 (Tania Paskova). This work was supported by the Laboratory Directed Research and Development program at Sandia National Laboratories. Sandia National Laboratories is a multimission laboratory managed and operated by National Technology and Engineering Solutions of Sandia, LLC, a wholly owned subsidiary of Honeywell International, Inc., for the U.S. Department of Energy's National Nuclear Security Administration under Contract No. DE-NA-0003525. The views expressed in the presentation do not necessarily represent the views of the U.S. Department of Energy or the United States Government. The authors are very grateful to their collaborators over the years, especially Chris Constantine, Dave Johnson, and Rohit Khanna of PlasmaTherm.

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